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By:

Date: September 27, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/623,843 Confirmation No. 6526

Applicant : Veronika Polei et al.

Filed : July 21, 2003

TC/A.U. : 2823

Examiner : Khiem D. Nguyen

Title: : METHOD FOR FABRICATING A BURIED BIT LINE FOR
A SEMICONDUCTOR MEMORY

Docket No. : P2002,0618

Customer No. : 24131

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

S i r :

In response to the Office action dated July 12, 2004, please
amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.